

Redmine - Task #225

Diseño de circuito XYZ

11/03/2015 04:19 PM - Joaquín Verástegui

Status:	Closed	Start date:	11/30/2015
Priority:	High	Due date:	12/07/2015
Assignee:	Joaquín Verástegui	% Done:	100%
Category:		Estimated time:	24.00 hours
Target version:	Pase a Producción	Spent time:	0.00 hour
Description			
Subtasks:			
Task # 226: Revisión de circuito impreso XYZ			Closed

History

#1 - 11/03/2015 05:45 PM - Joaquín Verástegui

- Status changed from New to In progress

#2 - 11/11/2015 10:25 AM - Cristian Castillo

- Target version set to Pase a Producción

#3 - 11/17/2015 08:12 AM - Joaquín Verástegui

- Status changed from In progress to Closed