

IDI - Instrumentación de radares - Milestone #1580

Divisor de PPS/Generador de pulso

01/29/2019 09:04 PM - Joaquín Verástegui

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|--|--------------------|------------------------|---------------|
| Status: | Closed | Start date: | 03/26/2019 |
| Priority: | Normal | Due date: | 09/16/2019 |
| Assignee: | Joaquín Verástegui | Estimated time: | 0.00 hour |
| Category: | | Spent time: | 0.00 hour |
| Target version: | | | |
| Description | | | |
| Subtasks: | | | |
| Task # 1672: Crear API para el SIR del Divisor de PPS | | | Closed |
| Task # 1674: Agregar comunicacion por bus de datos a componente de comunicacion y compo... | | | Closed |
| Task # 1675: Selección de funcionalidad para cuatro canales | | | Closed |
| Task # 1676: Implementar comandos nuevos de API Restful y modificar los anteriores | | | Closed |
| Task # 1677: Sincronizar pulso programado con la entrada de PPS | | | Closed |
| Task # 1711: Agregar LCD ILI9341 para visualizar parametros | | | Closed |
| Task # 1712: Modificar shield para distribuir conexiones | | | Closed |
| Task # 1713: Diseñar tarjeta de salidas de 10 MHz para el Trimble | | | Closed |
| Task # 1763: Agregar envio de datos del FPGA hacia la TIVA por SPI | | | Closed |
| Task # 1766: test bench para generador de pulsos | | | Closed |

History

#1 - 01/02/2024 03:02 PM - John Rojas

- Status changed from New to Closed